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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10040460	01-09-2002	257		2811	
**APPLICANTS: Kitahara Terumasa Yasuda Koichi: TTR					
**CONTINUING DATA VERIFIED: TTR					
** FOREIGN APPLICATIONS VERIFIED: TTR JAPAN 2001-183911 06/18/2001					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed 35 USC 119 conditions met: Verified and Acknowledged Examiners' initials		yes <input type="checkbox"/> no <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> TTR		ATTORNEY DOCKET NO 024016-00022	
TITLE : Semiconductor integrated circuit device					

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	
		Print Claim for O.G.	
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg.
		Print Fig.	
<input type="checkbox"/> TERMINAL DISCLAIMER		Application Examiner	
		PREPARED FOR ISSUE	
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